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Application No. <u>10/075178</u>	Prepared by <u>Jannotti</u>	Tracking Number <u>05963886</u>	
Examiner-GAU <u>SIK-8825</u>	Date <u>6/23/04</u>	Week Date <u>6/7/04</u>	
	No. of queries <u>1</u>	<u>IFW</u>	

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a. Serial No.	f. Foreign Priority	k. Print Claim(s)	<u>q. PTO-1449</u>
b. Applicant(s)	g. Disclaimer	l. Print Fig.	q. PTOL-85b
c. Continuing Data	h. Microfiche Appendix	m. Searched Column	r. Abstract
d. PCT	i. Title	n. PTO-270/328	s. Sheets/Figs
e. Domestic Priority	j. Claims Allowed	o. PTO-892	t. Other

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Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)			Complete If Known	
Application / C. nf. No.	10/075,178 / 7288			
Filing Dat	February 14, 2002			
First Named Invent r	Andy H. Gan			
Art Unit	2825			
Examiner Name	Binh C. Tat			
Attorney Docket Number	X-1068 US			
Sheet	1	of	7	

U.S. PATENT DOCUMENTS					
Examiner Initials *	Cite No. *	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code (if known)			
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		US- 5,142,625	08-25-92	Nakai	
		US- 4,855,669	08-08-89	Mahoney	
		US- 34,363	08-31-93	Freeman	
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		US- 5,600,845	02-04-97	Gilson	
		US- 5,574,930	11-12-96	Halverson Jr., et al.	

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		Country Code * -Number * -Kind Code * (if known)					
		EP 0315275 A2		10-05-89	LSI Logic		
		WO 93 25968 A1		12-23-93	Furtek		
		EP 0 905 906 A2		03-31-99	Lucent		
		EP 1 235 351 A1		08-28-02	Matsushita Electric		

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Applicant / Conf. No.	10/075,178 / 7288
		Filing Date	February 14, 2002
		First Named Inventor	Andy H. Gan
		Art Unit	2825
		Examiner Name	Binh C. Tat
Sheet	2	of	7
		Attorney Docket Number	X-1068 US

OTHER -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No*	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		SAYFE KIAEI et al., "VLSI DESIGN OF DYNAMICALLY RECONFIGURABLE ARRAY PROCESSOR-DRAP," IEEE, February 1989, pp. 2484-2488, V3.6, IEEE, 3 Park Avenue, 17th Floor, New York, NY 10016-5997	
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Sheet	3	of	7
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Application / C nt. No.	10/075,178 / 7288
Filing Date	February 14, 2002
First Name of Inventor	Andy H. Gan
Art Unit	2825
Examiner Name	Binh C. Tat
Attorney Docket Number	X-1068 IJS

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Examiner Initials *	Cite No.¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code (if known)			
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		US- 5,742,180	04-21-98	DeHon et al.	
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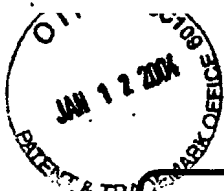
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				Filing Date	February 14, 2002
				First Named Inventor	Andy H. Gan
				Art Unit	2825
				Examiner Name	Binh C. Tat
Sheet	4	of	7	Attorney Docket Number	X-1068 US

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		XILINX, INC., "THE PROGRAMMABLE LOGIC DATA BOOK," 1994, Revised 1995, Xilinx, Inc., 2100 Logic Drive, San Jose, CA. 95124.	
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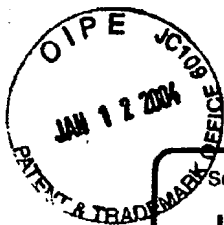
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